

FIG.1

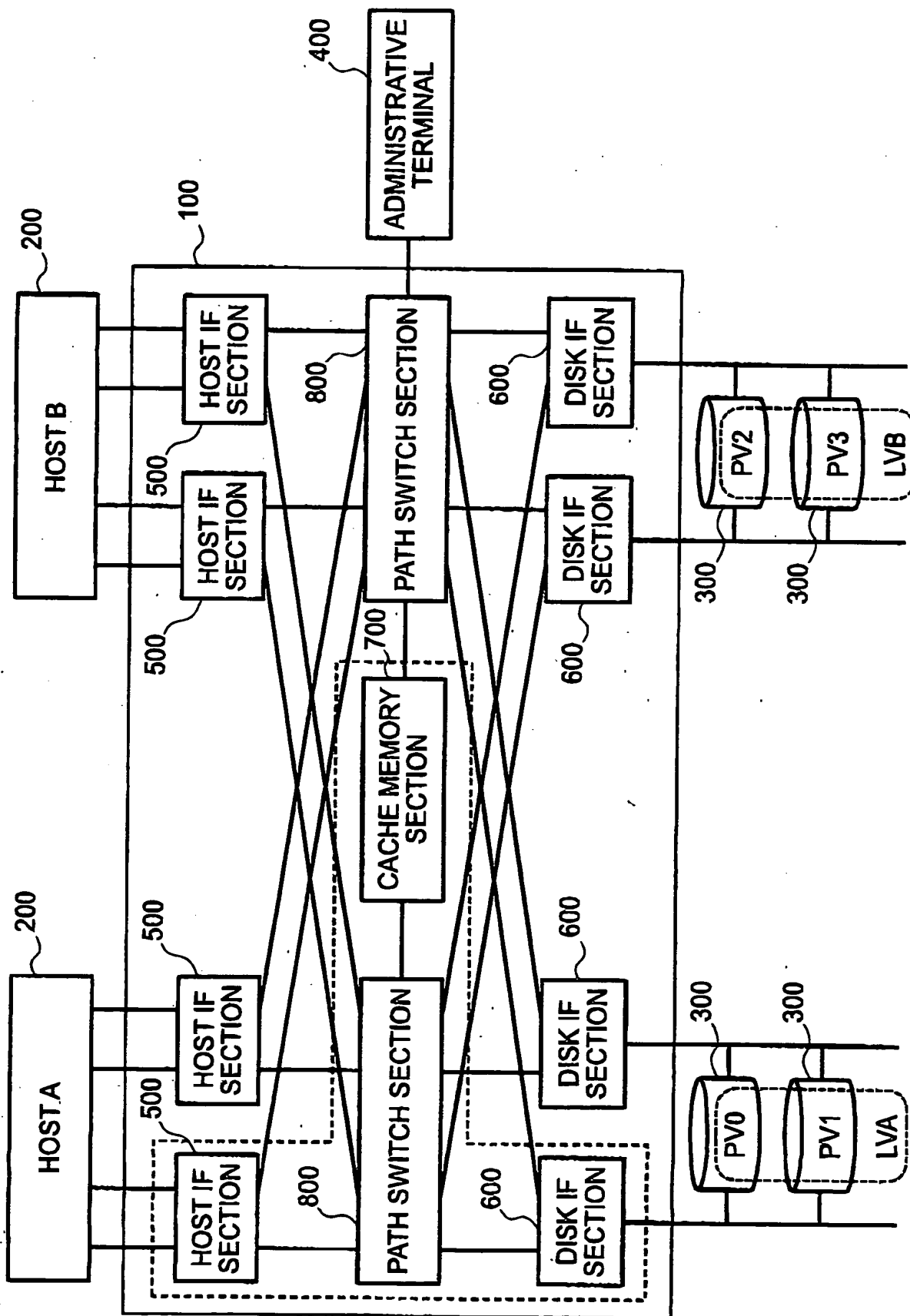


FIG.2

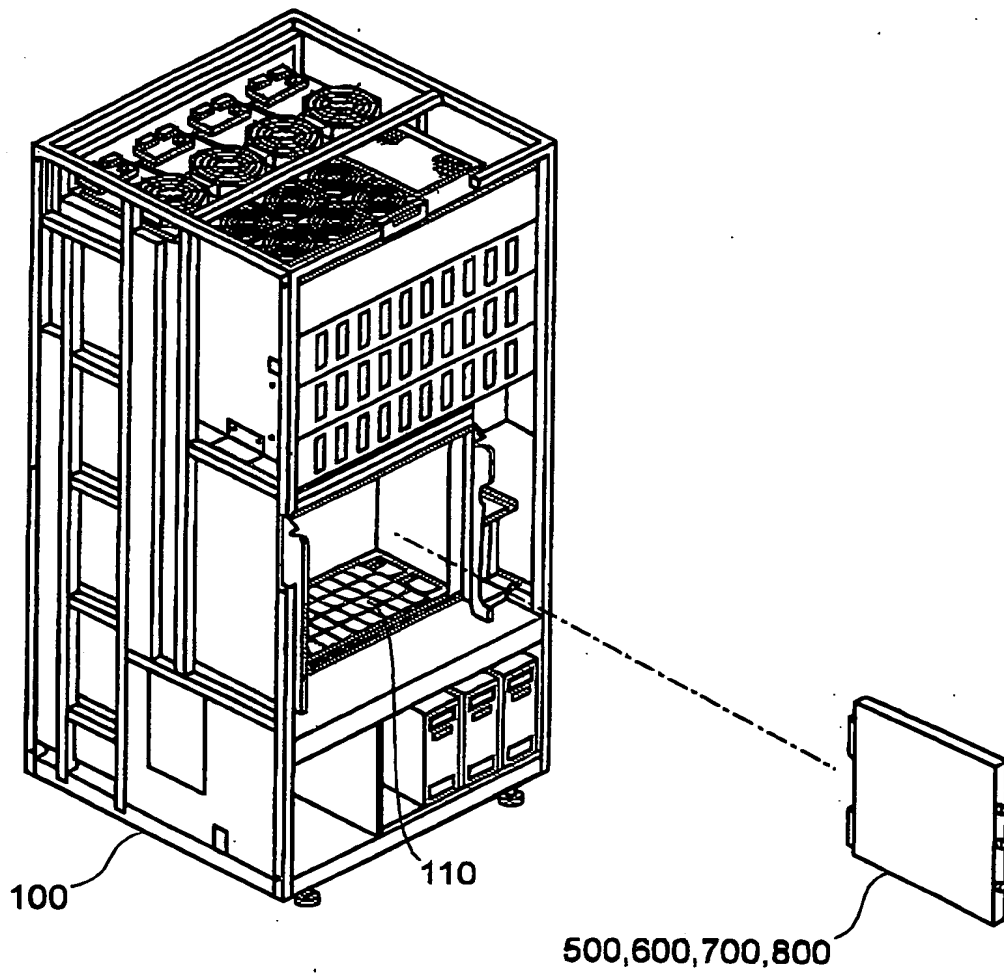


FIG.3

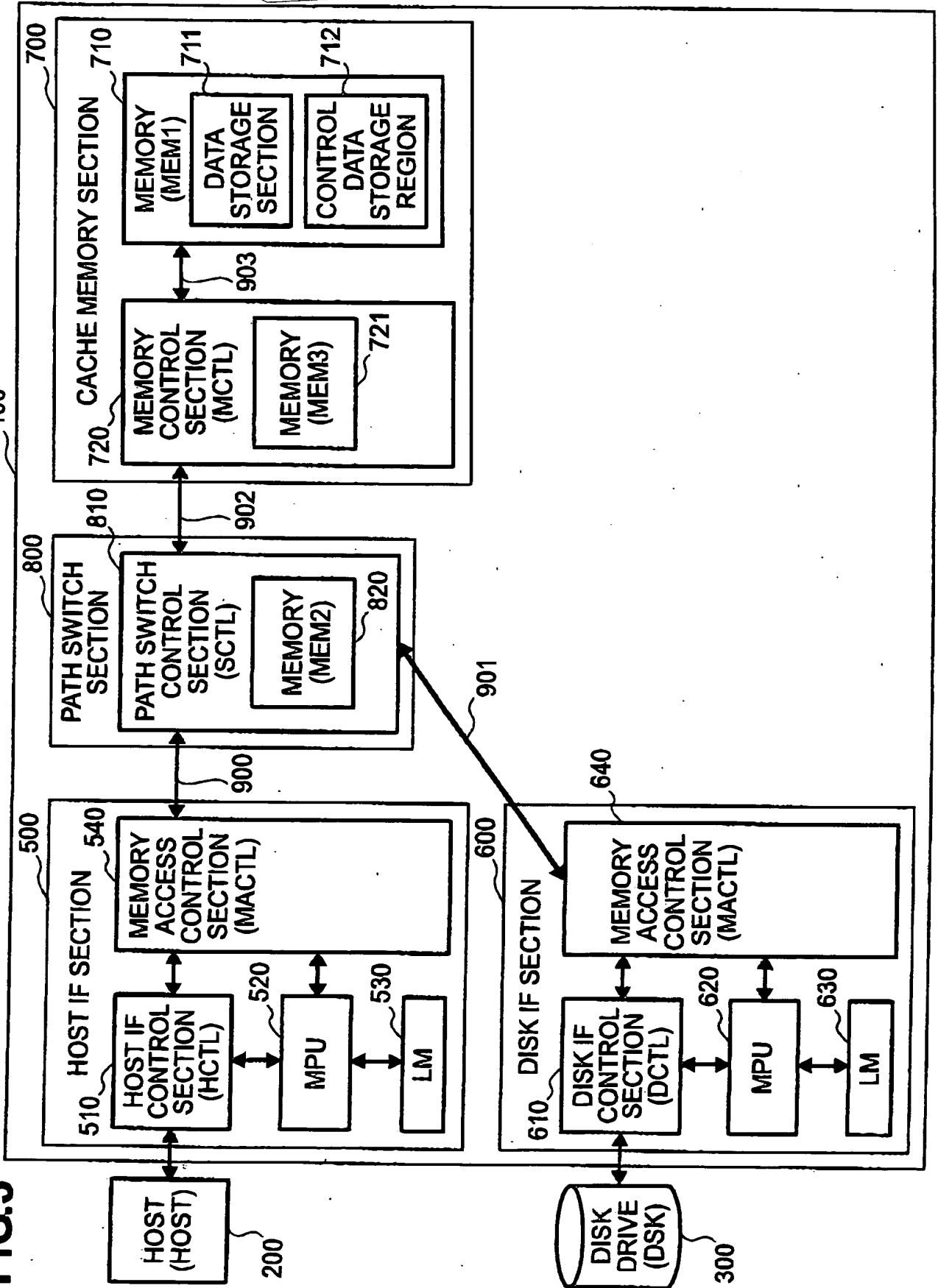


FIG.4

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HEADER SECTION(11)		DATA SECTION(12)	
BROAD CLASSIFICATION	LESS-BROAD CLASSIFICATION	DETAILS	
HEADER SECTION	ADDRESSER ID	ID OF ADDRESSER IF SECTION	
	ADDRESSEE ID	ID OF ADDRESSEE IF SECTION (e.g., 64 IF SECTIONS: 0-63(EACH IF SECTION), 128(EVERY IF SECTION), 255(MEMORY SECTION))	
	COMMUNICATIONS ATTRIBUTE	DATA TYPE(CONTROL DATA OR DATA)	
		ACCESS/NO ACCESS TO MEMORY IN SWITCH	
		ACCESS/NO ACCESS TO MEMORY IN MEMORY CONTROL SECTION	
		TARGET NODE(ENTIRE MPU COMMUNICATIONS, SPECIFIC MPU COMMUNICATIONS, CACHE MEMORY COMMUNICATIONS)	
		PRIORITY INFORMATION	
DATA SECTION	ACCESS ADDRESS	ACCESSING ADDRESS e.g., MEMORY	
	DATA LENGTH	ACCESSING DATA LENGTH	
	DATA/CONTROL DATA	ACTUAL DATA/CONTROL DATA	
	CHECK CODE	DATA PROTECTION CODE	

FIG.5

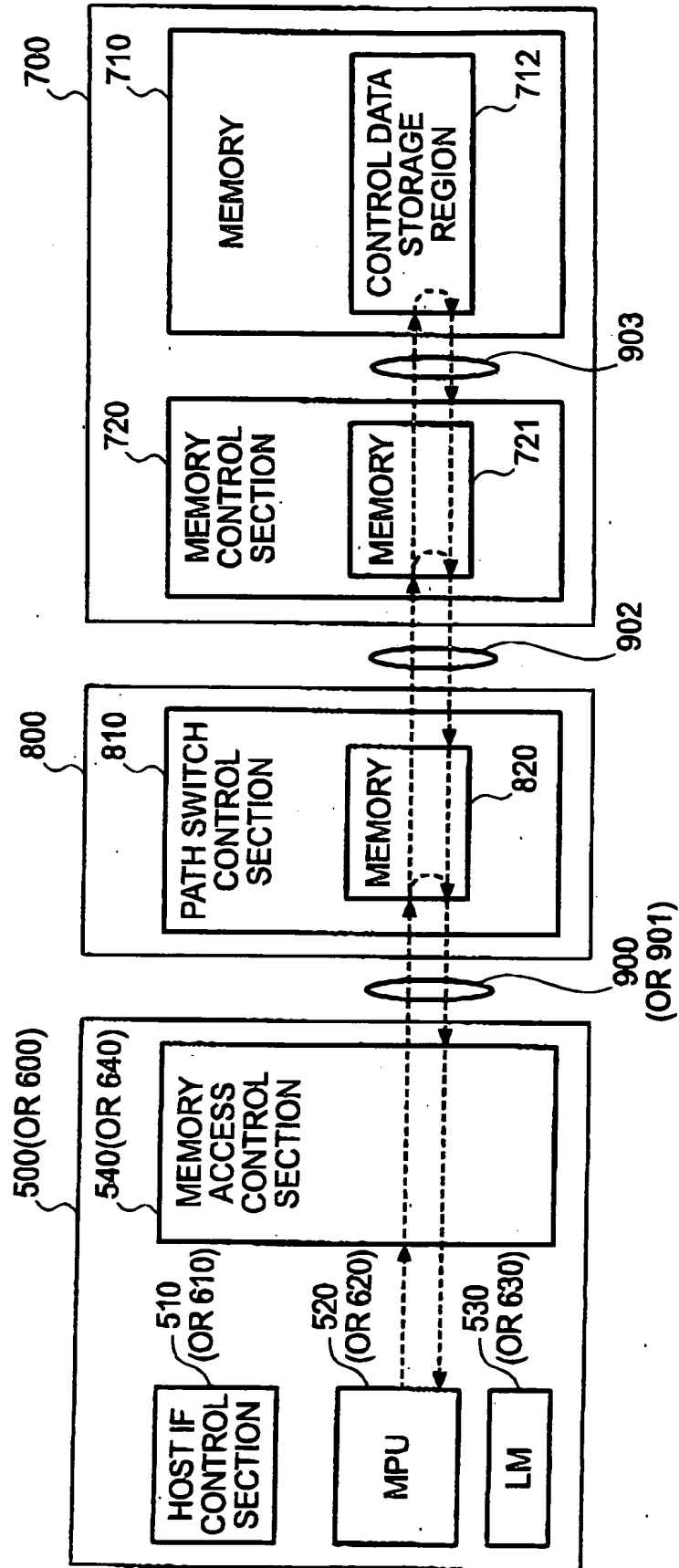


FIG.6

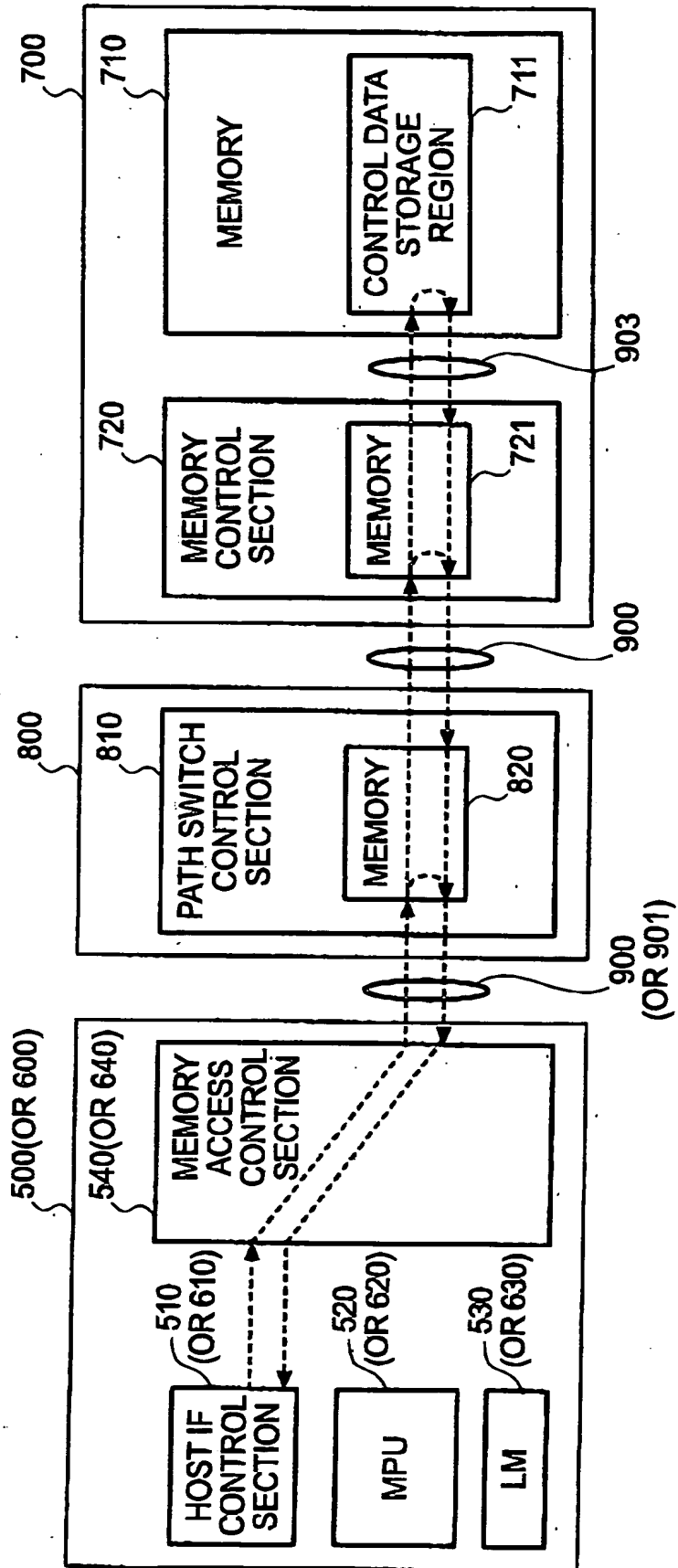


FIG.7

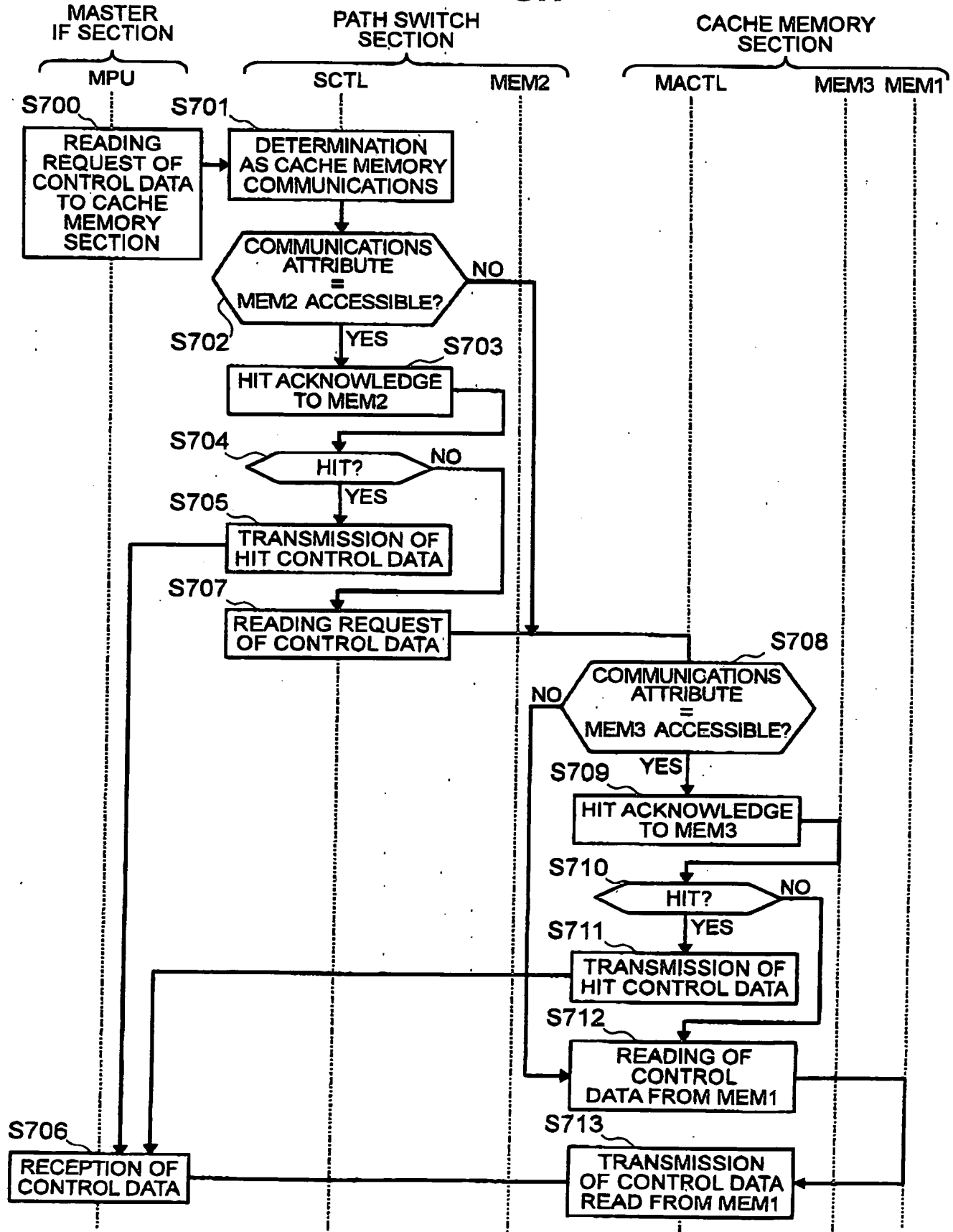


FIG.8

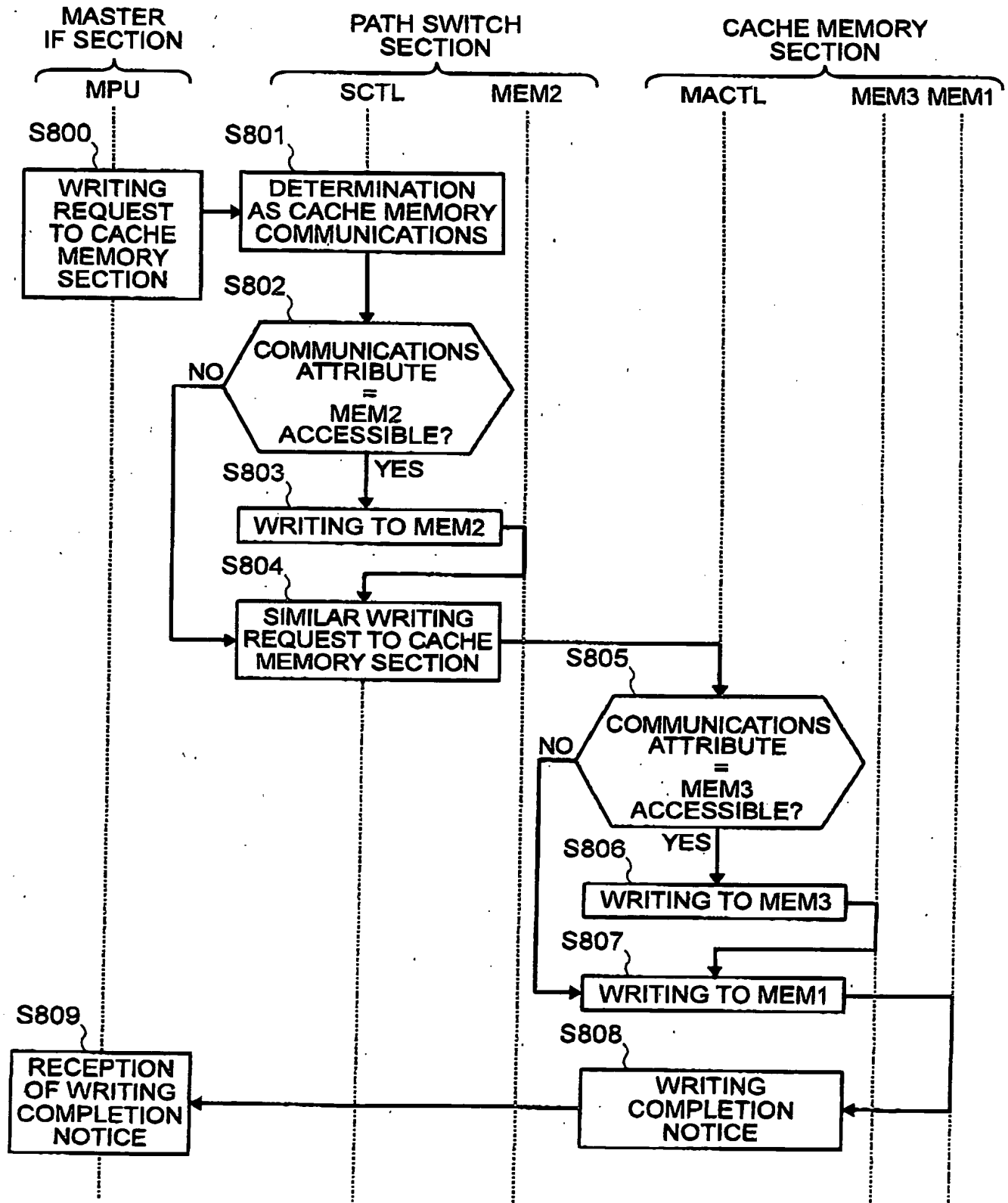


FIG.9

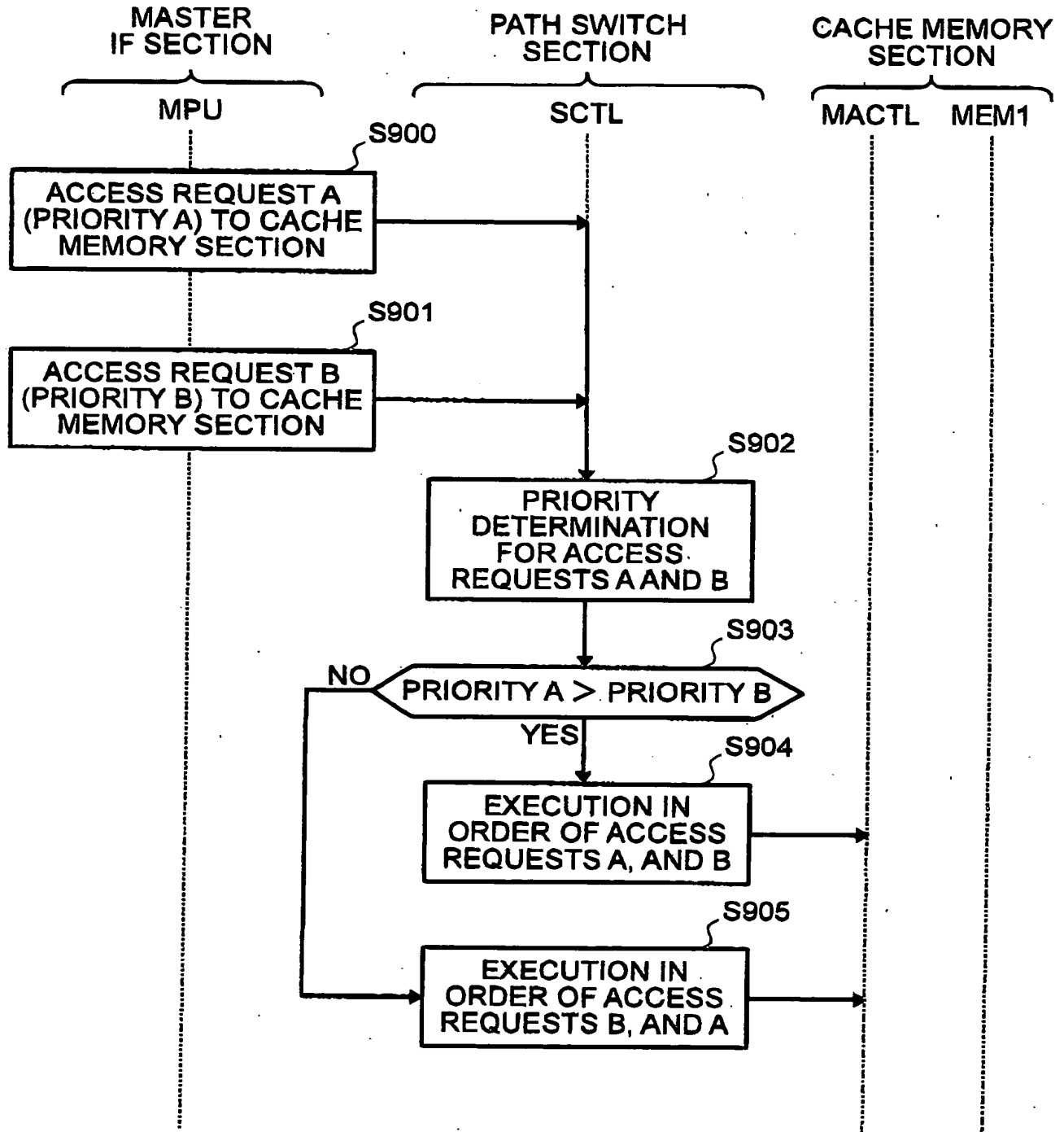


FIG.10

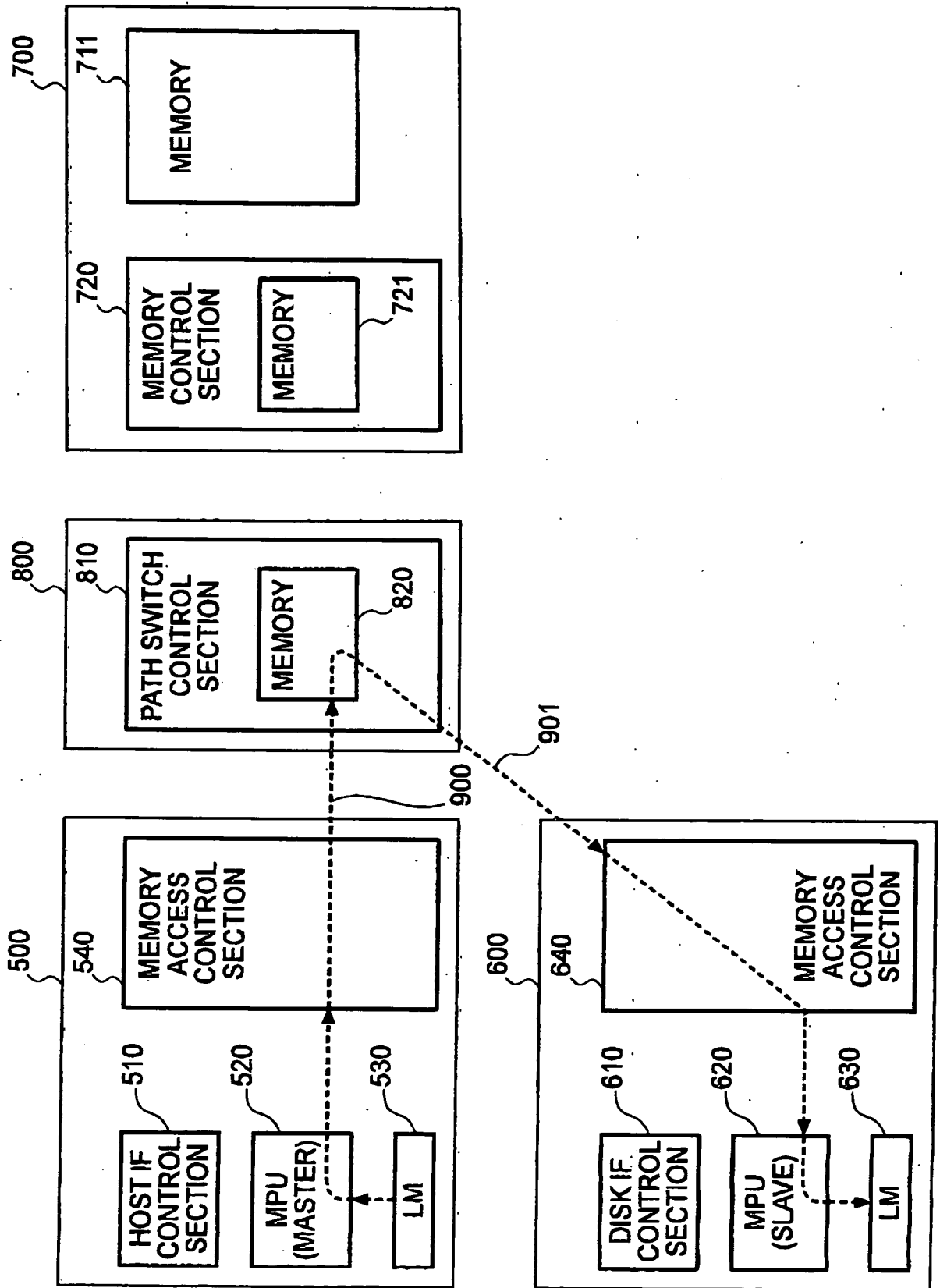


FIG.11

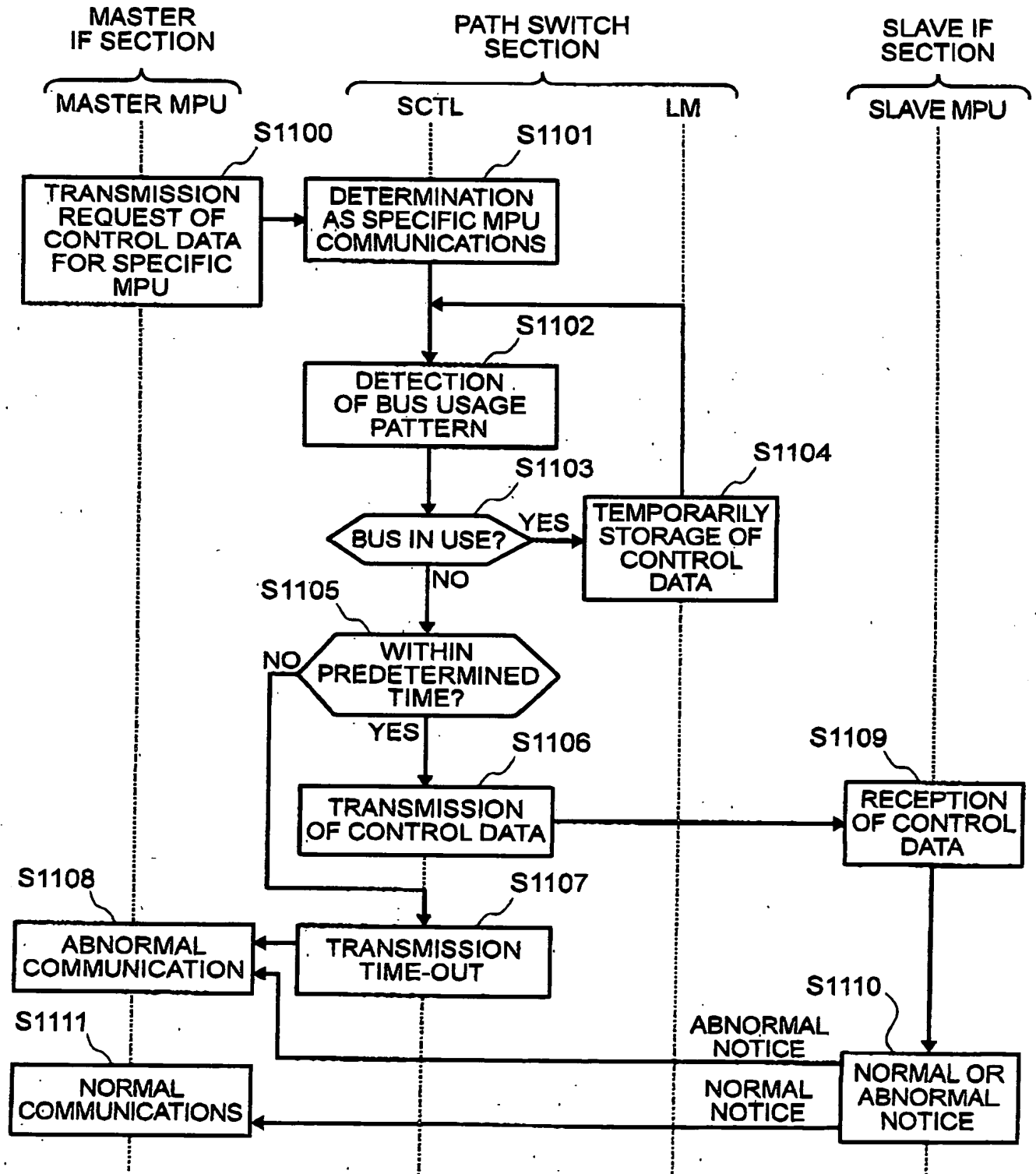


FIG.12

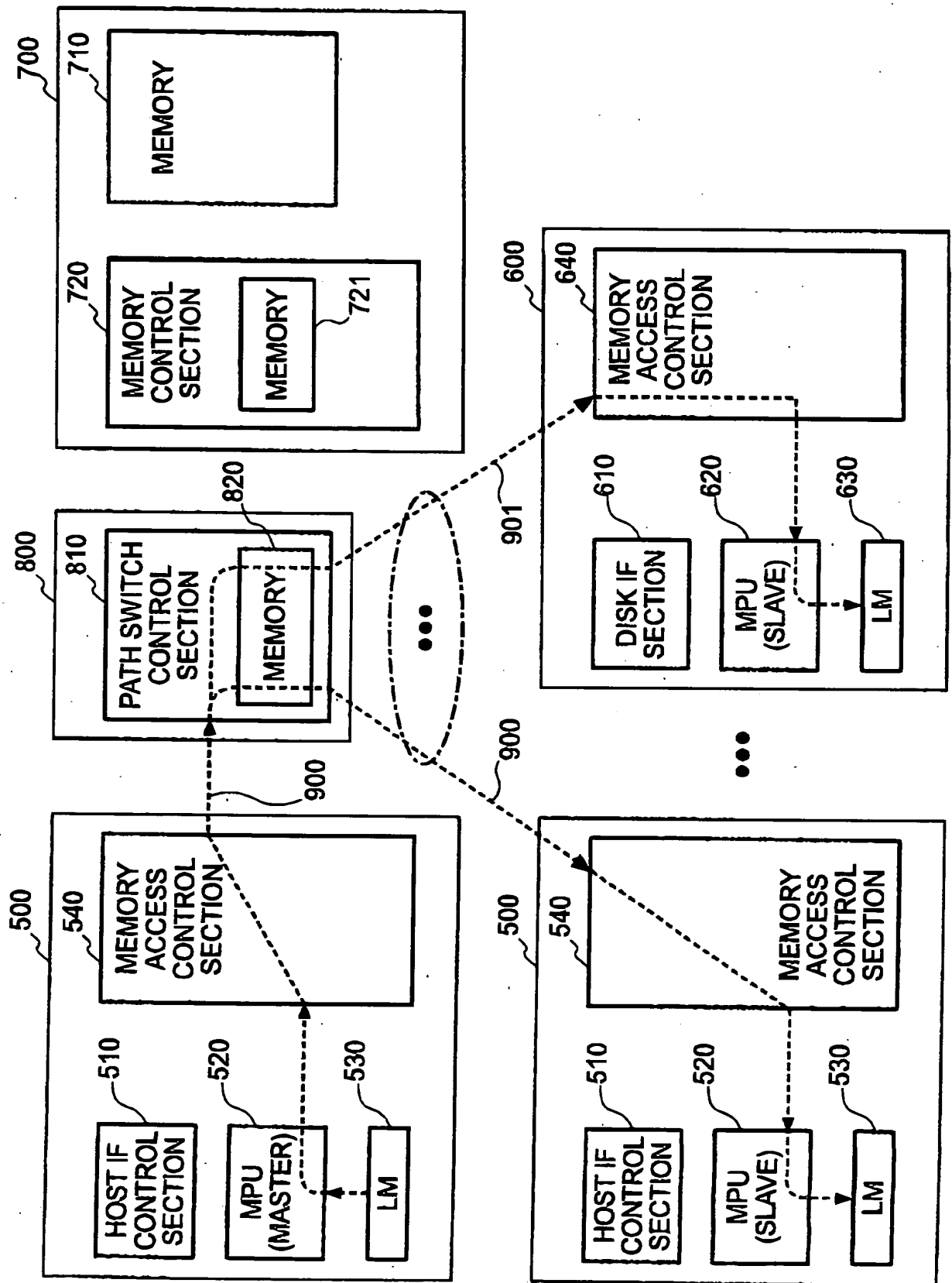


FIG.13

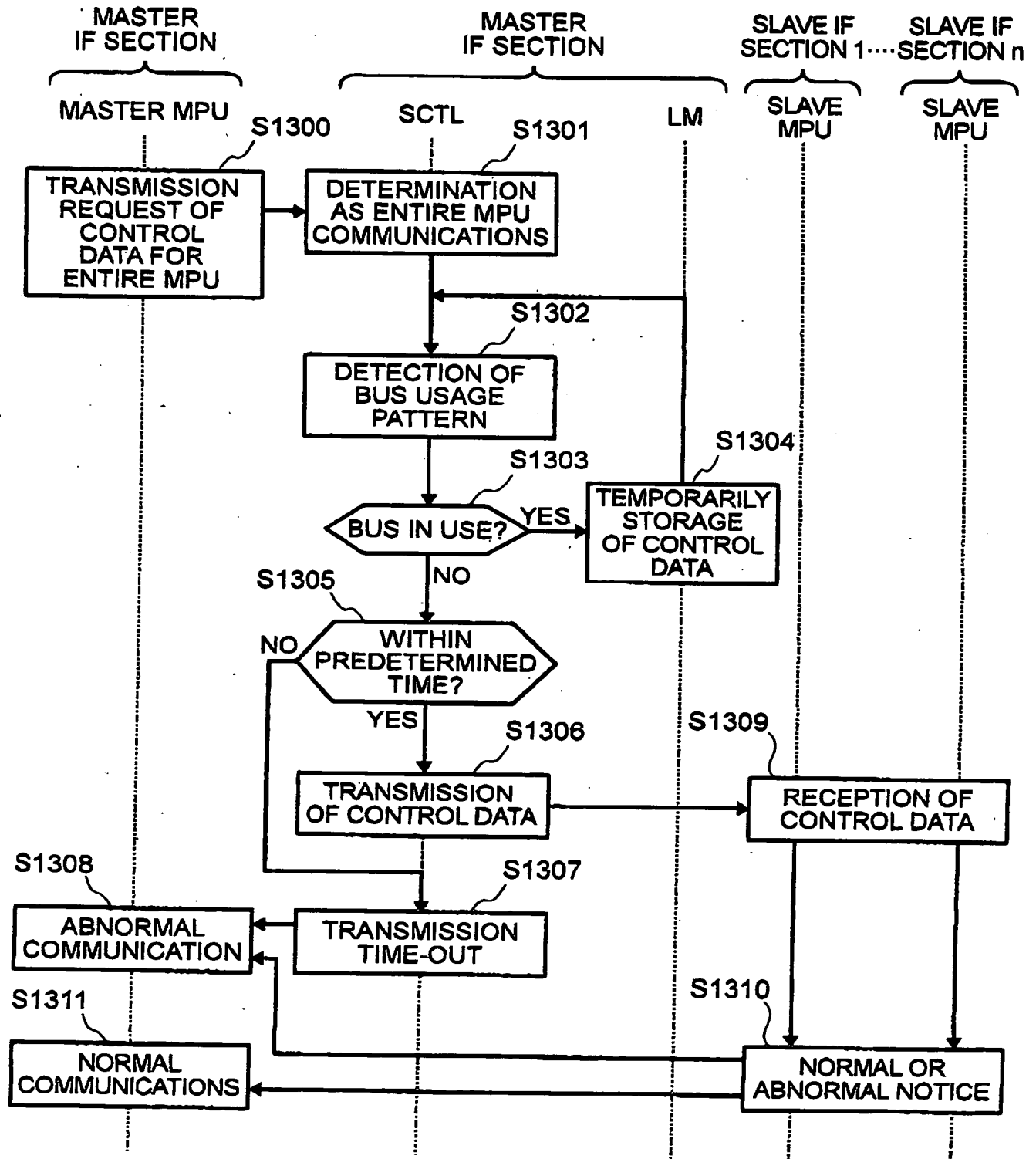


FIG.14

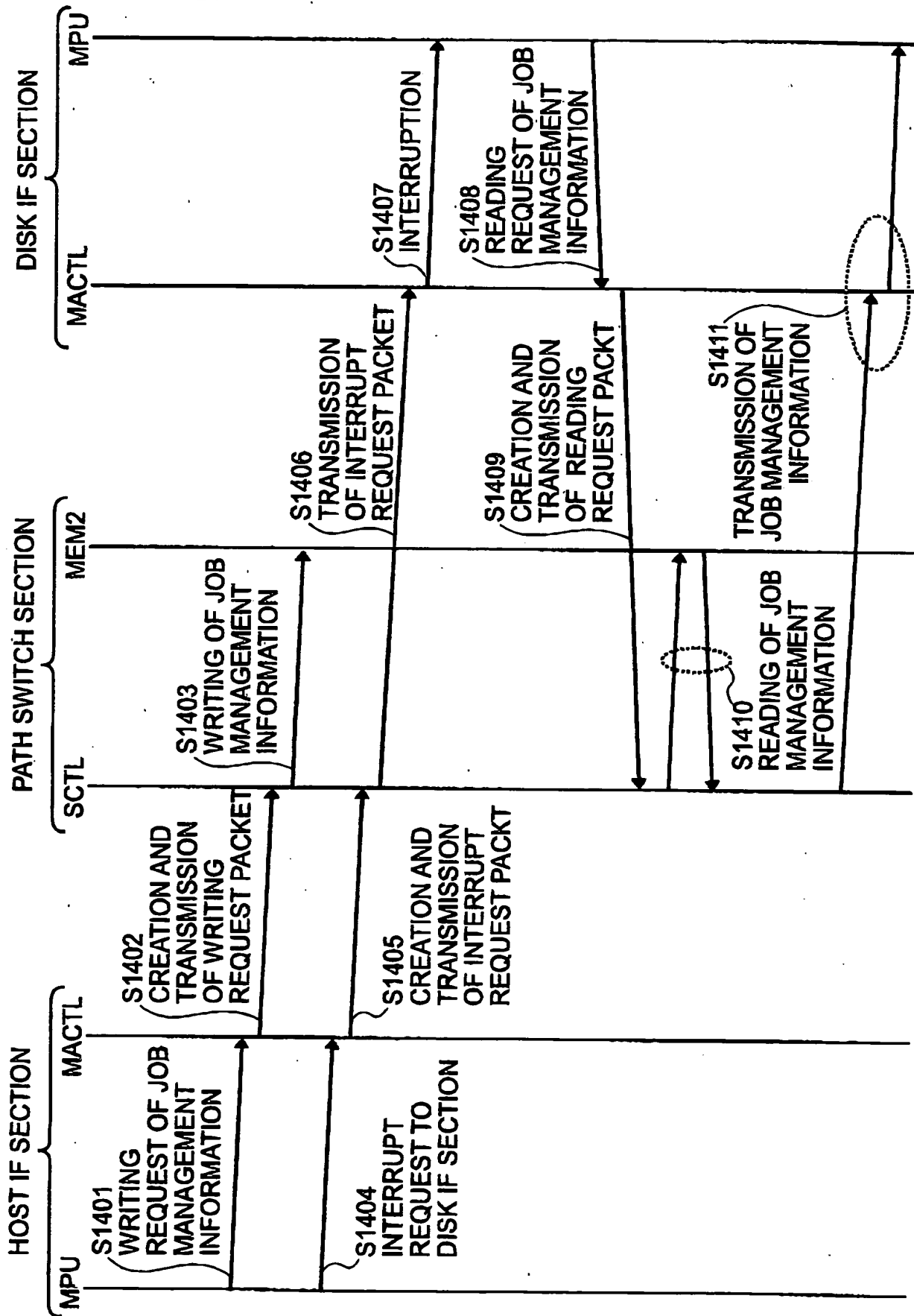


FIG.15

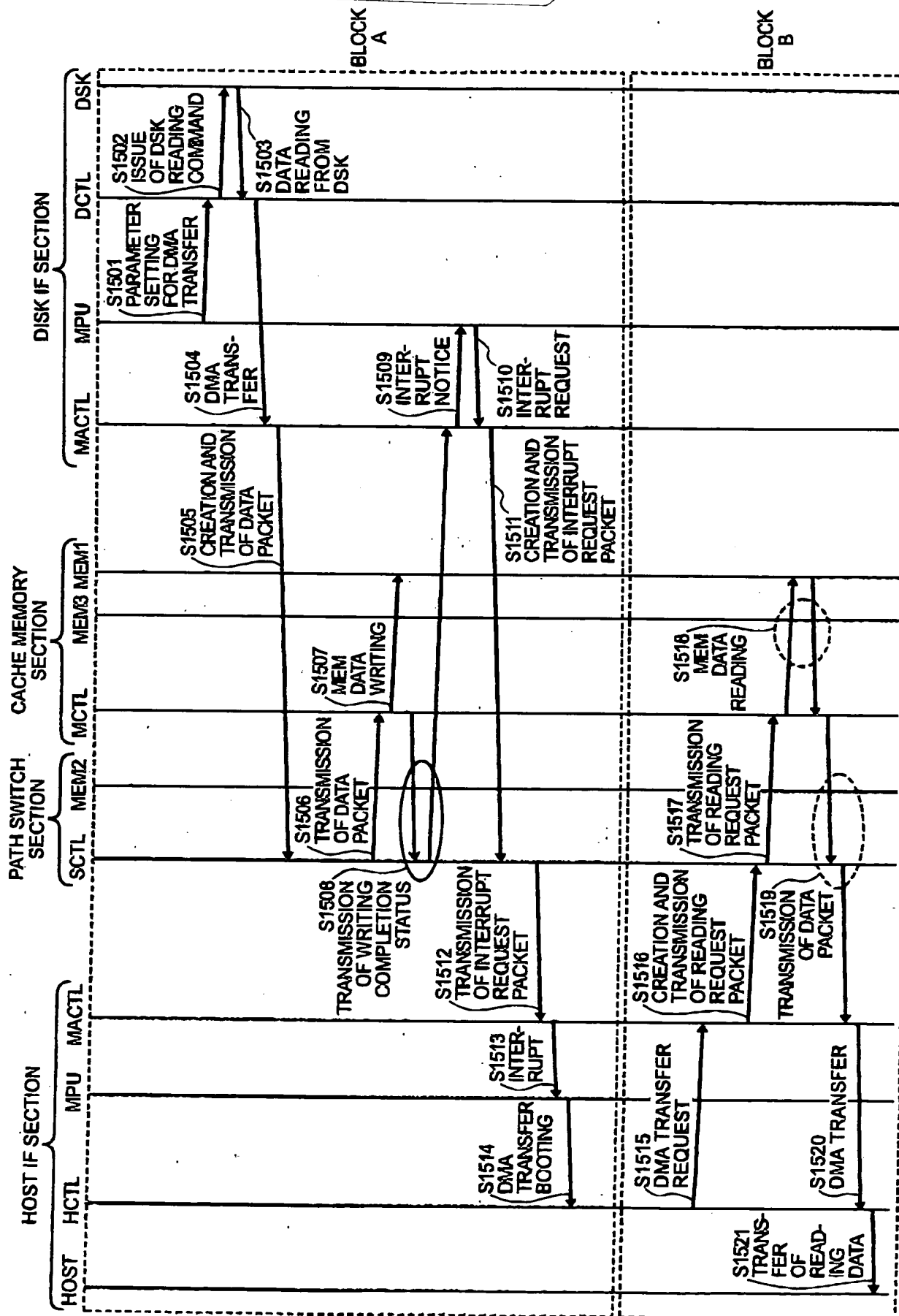


FIG.16

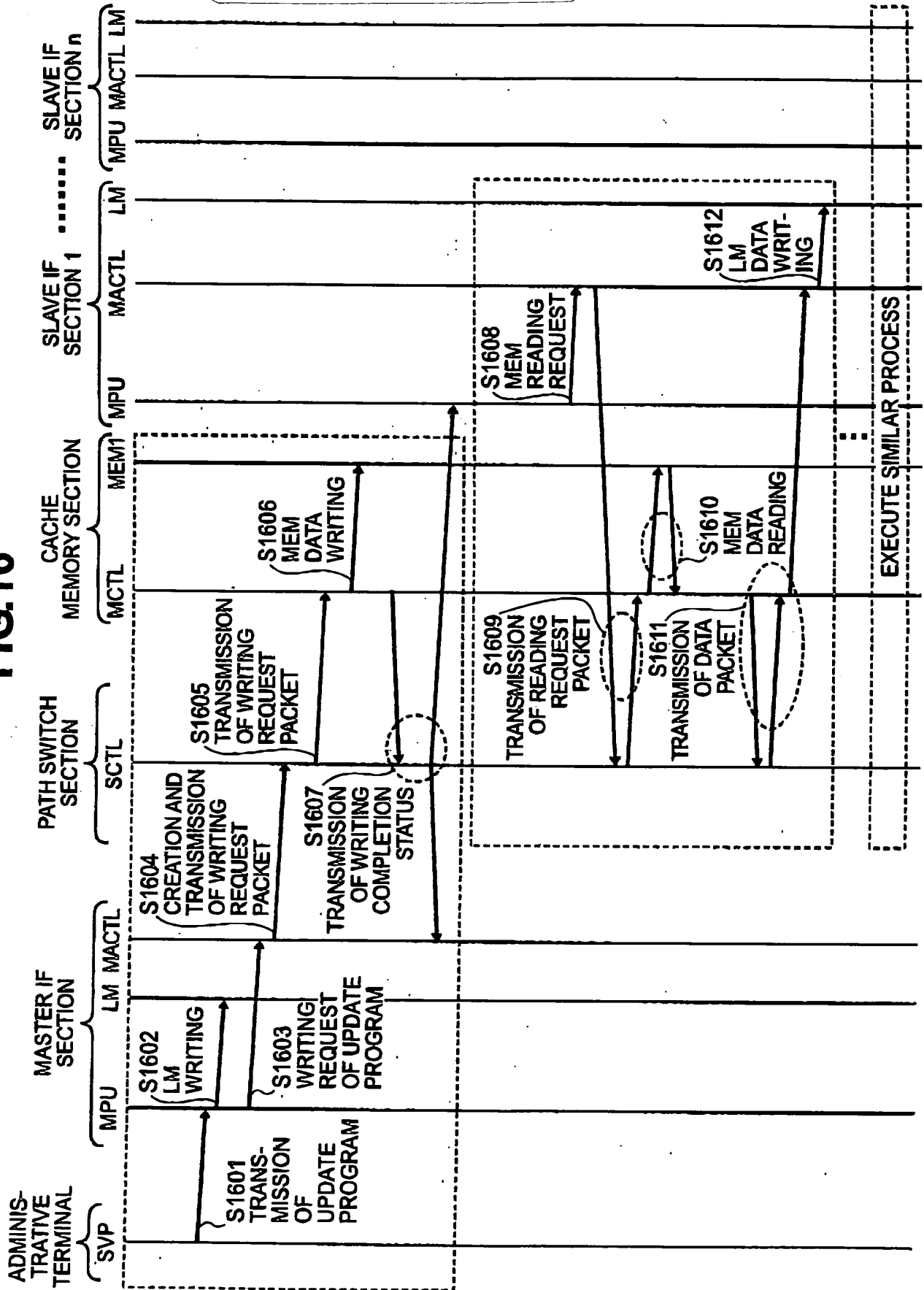


FIG.17

